

R. Carter

2. (Amended) A logical circuit designing device, comprising:
a logical circuit storage unit storing a logical circuit;
a transmission line circuit storage unit storing a transmission line circuit corresponding to the logical circuit stored in the logical circuit storage unit;
a transmission line circuit editing unit editing the transmission line circuit stored in the transmission line circuit storage unit; and
a logical circuit modification unit modifying the corresponding logical circuit based on the transmission line circuit edited by the transmission line circuit editing unit, and
wherein the logic circuit is automatically modified when the transmission line circuit is re-edited.

3. (Amended) A logical circuit designing device, comprising:
a logical circuit storage unit storing a logical circuit;
a transmission line circuit generation unit generating a transmission line circuit based on the logical circuit stored in the logical circuit storage unit;
a transmission line circuit storage unit storing the transmission line circuit generated by the transmission line circuit generation unit;
a transmission line circuit editing unit editing the transmission line circuit stored in the transmission line circuit storage unit; and
a logical circuit modification unit modifying the corresponding logical circuit based on the transmission line circuit edited by the transmission line circuit editing unit, and
wherein the logic circuit is automatically modified when the transmission line circuit is re-edited.

7. (Amended) A logical circuit designing device, comprising:
a logical circuit storage unit storing a logical circuit;
a transmission line circuit generation unit generating a transmission line circuit based on the logical circuit stored in the logical circuit storage unit;
a transmission line circuit storage unit storing the transmission line circuit generated by the transmission line circuit generation unit; and
a deletion designation table storing deletion information of a passive component composing a logical circuit, and
wherein said transmission line circuit generation unit generates a transmission line circuit

by deleting the passive component based on the passive component deletion information stored in the deletion designation table.

11. (Amended) A logical circuit designing device, comprising:
a logical circuit storage unit storing a logical circuit;
a transmission line circuit generation unit generating a transmission line circuit based on the logical circuit stored in the logical circuit storage unit;
a transmission line circuit storage unit storing the transmission line circuit generated by the transmission line circuit generation unit;
a transmission line circuit editing unit editing the transmission line circuit stored in the transmission line circuit storage unit;
a logical circuit modification unit modifying the corresponding logical circuit based on the transmission line circuit edited by the transmission line circuit editing unit; and
a deletion designation table storing deletion information of a passive component composing a logical circuit, and wherein said transmission line circuit generation unit generates a transmission line circuit by deleting the passive component based on the passive component deletion information stored in the deletion designation table.

17. (Amended) A logical circuit designing method, comprising:
generating transmission line circuit data suitable for transmission line circuit analysis based on a logical circuit stored in a logical circuit database; and
storing the generated transmission line circuit data in a transmission line circuit database.

18. (Amended) A logical circuit designing method, comprising:
editing the transmission line circuit stored in the transmission line circuit database; and
modifying a logical circuit corresponding to the transmission line circuit based on the edited transmission line circuit, and
wherein the logic circuit is automatically modified when the transmission line circuit is re-edited.

19. (Amended) A logical circuit designing method, comprising:
generating a transmission line circuit based on a logical circuit stored in a logical circuit

database;

storing the generated transmission line circuit in a transmission line circuit database
editing the transmission line circuit stored in the transmission line circuit database; and
modifying the generated logical circuit based on the edited transmission line circuit, and
wherein the logic circuit is automatically modified when the transmission line circuit is re-
edited.

23. (Amended) A logical circuit designing method, comprising:
generating a transmission line circuit based on a logical circuit stored in a logical circuit
database; and

storing the generated transmission line circuit in a transmission line circuit database, and
wherein the transmission line circuit is generated by deleting a passive component based
on passive component deletion information stored in a deletion designation table storing deletion
information of passive components composing a logical circuit, in said generating.

27. (Amended) A logical circuit designing method, comprising:
generating a transmission line circuit based on a logical circuit stored in a logical circuit
database;

storing the generated transmission line circuit in a transmission line circuit database
editing the transmission line circuit stored in the transmission line circuit database; and
modifying the generated logical circuit based on the edited transmission line circuit, and
wherein the transmission line circuit is generated by deleting a passive component based
on deletion information of the passive component stored in a deletion designation table storing
deletion information of passive components composing a logical circuit, in said generating.

33. (Amended) A computer-readable storage medium which stores a logical circuit
designing program for enabling a computer, comprising:

generating transmission line circuit data suitable for transmission line circuit analysis
based on a logical circuit stored in a logical circuit database; and
storing the generated transmission line circuit data in a transmission line circuit
database.

34. (Amended) A computer-readable storage medium which stores a logical circuit

designing program for enabling a computer, comprising:

editing the transmission line circuit stored in the transmission line circuit database; and
modifying a logical circuit corresponding to the transmission line circuit based on the
edited transmission line circuit, and
wherein the logic circuit is automatically modified when the transmission line circuit is re-
edited.

*A¹
Article*
35. (Amended) A computer-readable storage medium which stores a logical circuit
designing program for enabling a computer, comprising:

generating a transmission line circuit based on a logical circuit stored in a logical circuit
database;
storing the generated transmission line circuit in a transmission line circuit database;
editing the transmission line circuit stored in the transmission line circuit database; and
modifying a logical circuit corresponding to the transmission line circuit based on the
edited transmission line circuit, and
wherein the logic circuit is automatically modified when the transmission line circuit is re-
edited.

39. (Amended) A computer-readable storage medium which stores a logical circuit
designing program for enabling a computer, comprising:

A⁸
generating a transmission line circuit based on a logical circuit stored in a logical circuit
database; and
storing the generated transmission line circuit in a transmission line circuit database, and
wherein the transmission line circuit is generated by deleting a passive component based
on passive component addition information stored in an addition designation table storing
deletion information of passive components composing a logical circuit, in said generating.

43. (Amended) A computer-readable storage medium which stores a logical circuit
designing program for enabling a computer, comprising:

A⁹
generating a transmission line circuit based on a logical circuit stored in a logical circuit
database;
storing the generated transmission line circuit in a transmission line circuit database;
editing the transmission line circuit stored in the transmission line circuit database; and

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modifying a logical circuit corresponding to the transmission line circuit based on the edited transmission line circuit, and

wherein the transmission line circuit is generated by deleting a passive component based on passive component addition information stored in an addition designation table storing deletion information of passive components composing a logical circuit in said generating.

49. (Amended) A logical circuit designing program for enabling a computer, comprising:

generating transmission line circuit data suitable for transmission line circuit analysis based on a logical circuit stored in a logical circuit database; and

storing the generated transmission line circuit data in a transmission line circuit database.

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50. (Amended) A logical circuit designing program for enabling a computer, comprising:

editing the transmission line circuit stored in the transmission line circuit database; and
modifying a logical circuit corresponding to the transmission line circuit based on the edited transmission line circuit, and

wherein the logic circuit is automatically modified when the transmission line circuit is re-edited.

51. (Amended) A logical circuit designing program for enabling a computer, comprising:

generating a transmission line circuit based on a logical circuit stored in a logical circuit database;

storing the generated transmission line circuit in a transmission line circuit database;
editing the transmission line circuit stored in the transmission line circuit database; and
modifying a logical circuit corresponding to the transmission line circuit based on the edited transmission line circuit, and

wherein the logic circuit is automatically modified when the transmission line circuit is re-edited.

55. (Amended) A logical circuit designing program for enabling a computer,

comprising:

generating a transmission line circuit based on a logical circuit stored in a logical circuit database; and

storing the generated transmission line circuit in a transmission line circuit database, and
wherein the transmission line circuit is generated by deleting a passive component based
on passive component addition information stored in an addition designation table storing
deletion information of passive components composing a logical circuit, in said generating.

59. (Amended) A logical circuit designing program for enabling a computer,
comprising:

generating a transmission line circuit based on a logical circuit stored in a logical circuit database;

storing the generated transmission line circuit in a transmission line circuit database;
editing the transmission line circuit stored in the transmission line circuit database; and
modifying a logical circuit corresponding to the transmission line circuit based on the
edited transmission line circuit, and

wherein the transmission line circuit is generated by deleting a passive component based
on passive component addition information stored in an addition designation table storing
deletion information of passive components composing a logical circuit, in said generating.

65. (Amended) A logical circuit designing device, comprising:
logical circuit storage means for storing a logical circuit;
transmission line circuit generation means for generating transmission line circuit data
suitable for transmission line circuit analysis based on the logical circuit stored in the logical
circuit storage means; and
transmission line circuit storage means for storing the transmission line circuit data
generated by the transmission line circuit generation means.

REMARKS

In the Office Action mailed February 27, 2003 the Examiner noted that claims 1-65 were pending, objected to claims 7, 11, 15, 23, 27, 31, 39, 43, 47, 55, 59, and 63 and rejected claims 1-6, 8-10, 12-14, 16-22, 24-26, 28-30, 32-38, 40-42, 44-46, 48-54, 56-58, 60-62, 64 and 65. Claims 1, 2, 3, 7, 11, 17-19, 23, 27, 33-35, 39, 43, 49-51, 55, 59 and 65 have been amended,